



⋮

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit

⋮

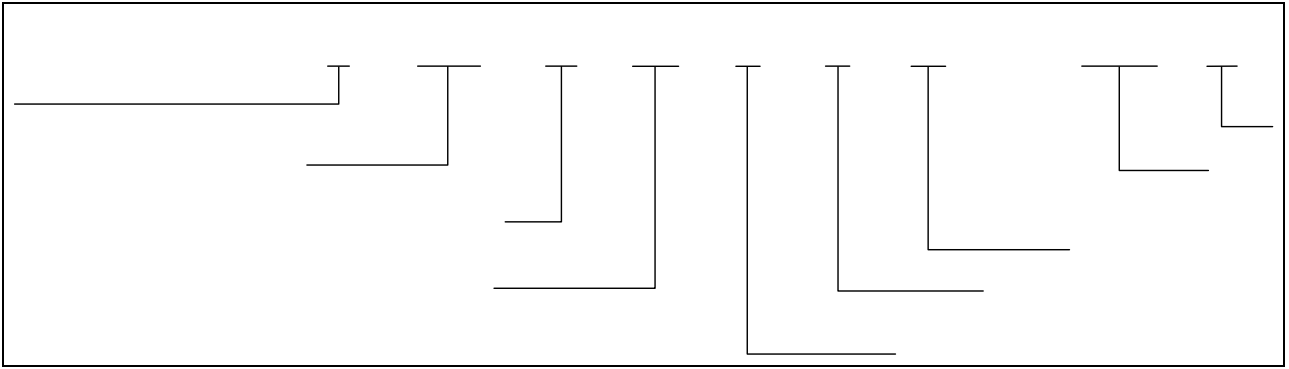




FIG.12:

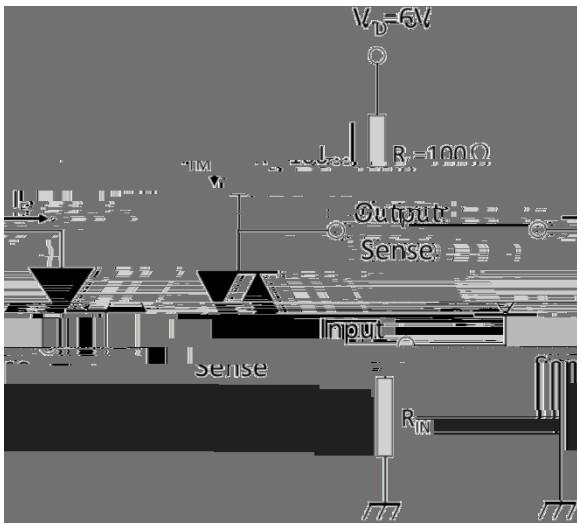


FIG.13:

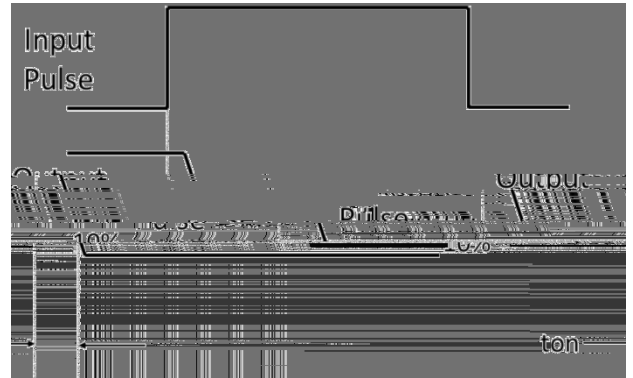


Fig.14: Test Circuits of dV/dt

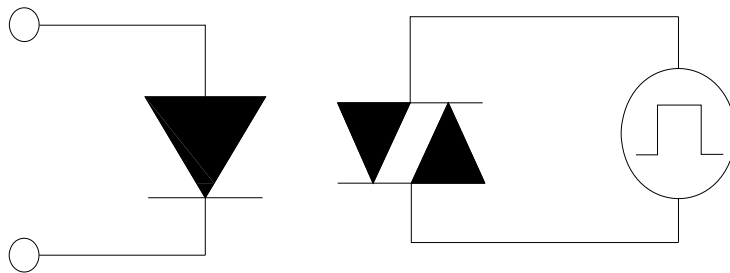


Fig.15: Waveforms of dV/dt

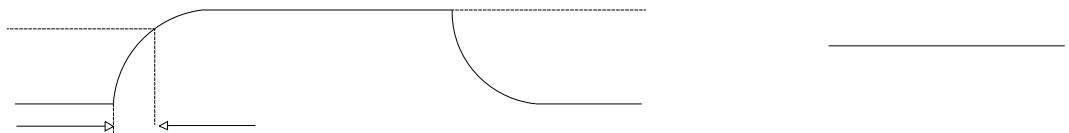
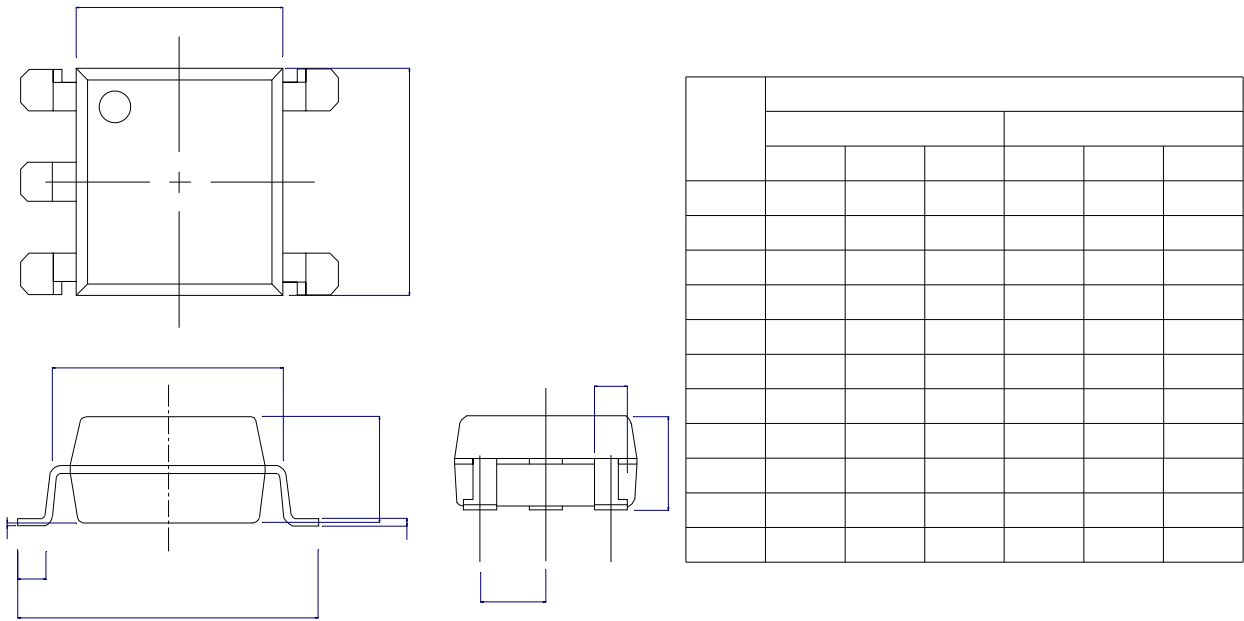
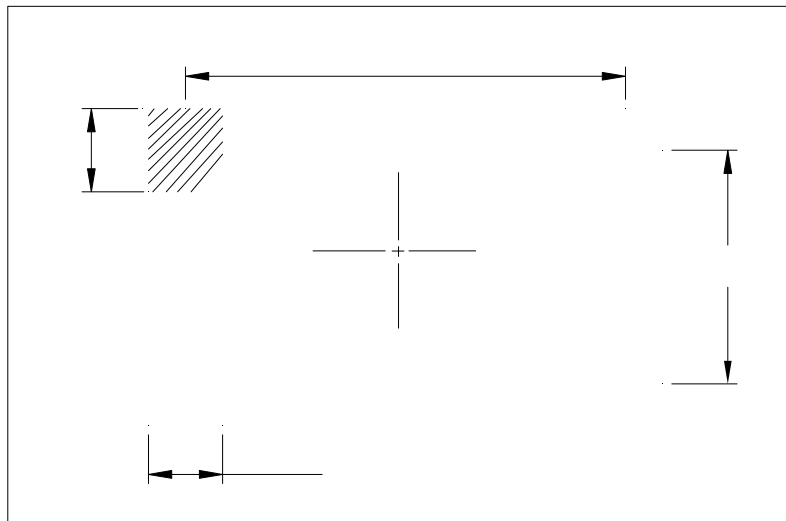


FIG.16:

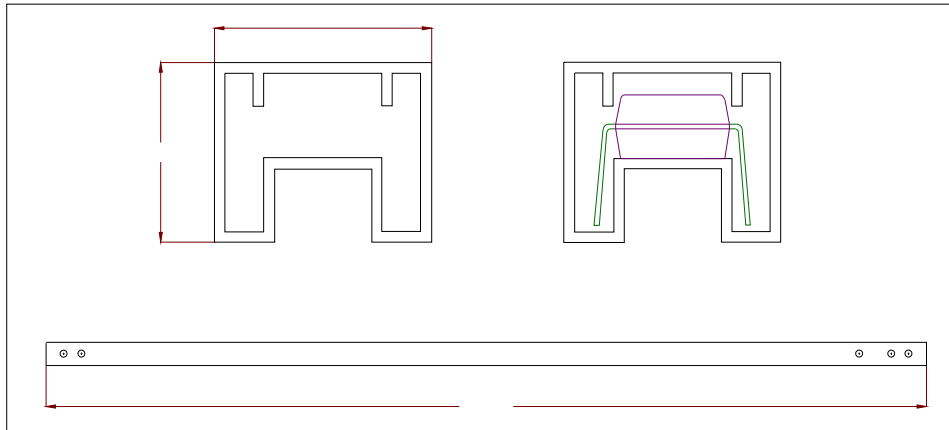
Option SMD Type:



Option SMD



Standard DIP



Option S/L

