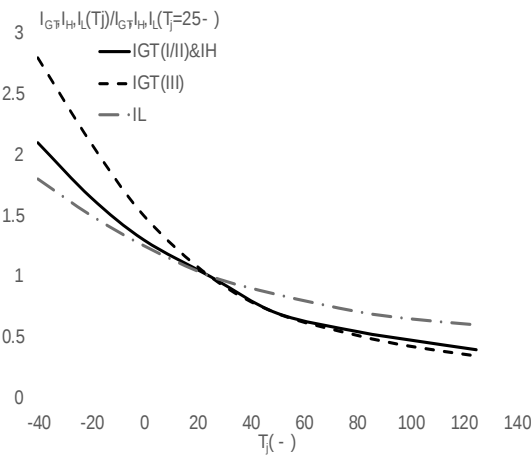


FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

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FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature



ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery
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PACKAGE MECHANICAL DATA

Ref.	Dimensions				
	Millimeters			Inches	
	Min.	Typ.	Max.	Min.	Typ.

Ref.

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