

**T0810H-6F 8A TRIAC**

Rev.A.1.1

The T0810H-6F triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Compared to traditional triacs, T0810H-6F provides a very high switching capability up to junction temperatures of 150°C. It can be driven directly through the MCU I/O port. By using an external plastic package, T0810H-6F provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-150	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	600	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	600	V
RMS on-state current ($T_C \leq 113^\circ\text{C}$)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	80	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		88	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	32	A^2s

Critical rate of rise of on-state current
($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=150^\circ\text{C}$)

Peak gate current (tp=20μs , Tj=150)	IGM	4	A
Average gate power dissipation (Tj=150)	PG(AV)	1	W
Peak gate power	PGM	10	W
Peak pulse voltage (Tj=25 ; non-repetitive,off-state;FIG.7)	Vpp	2.5	kV

(Tj=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value	Unit
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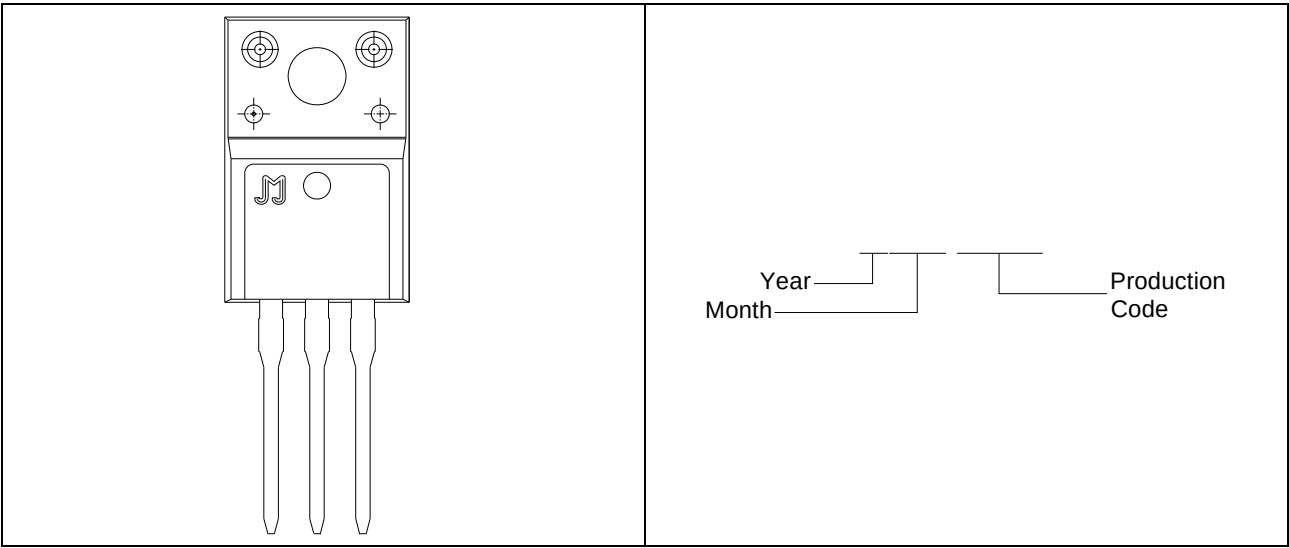
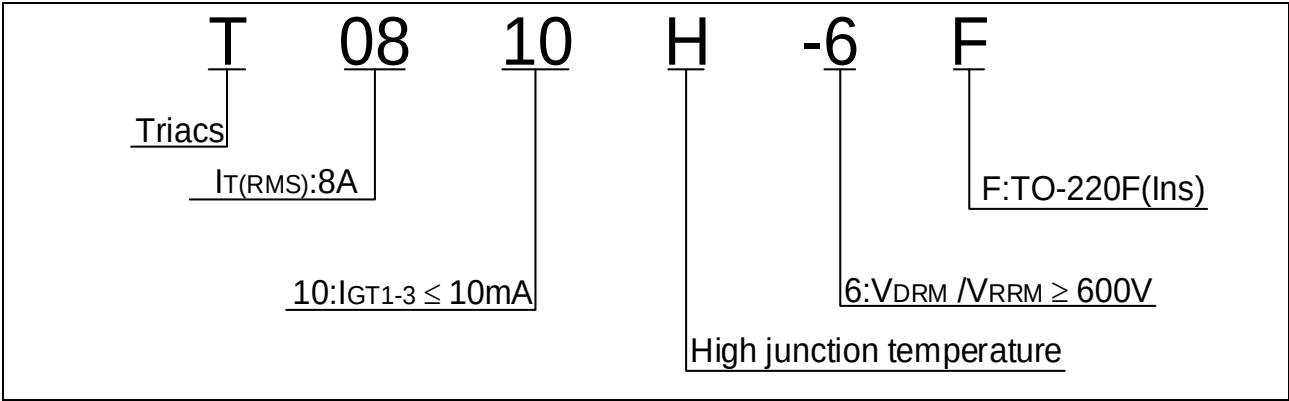


FIG.1: Maximum power dissipation versus RMS on-state current

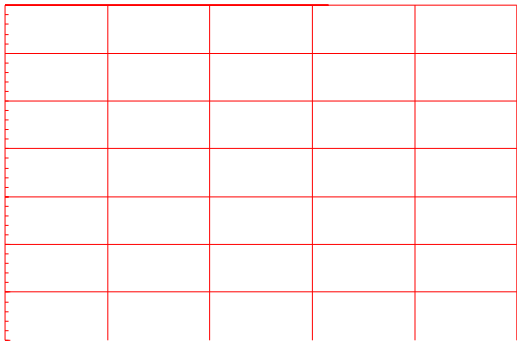


FIG.2: RMS on-state current versus case temperature

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
T0810H-6F	600	10	TO-220F(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated
Otc.11, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA



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