



JST136Q-800D 4A TRAC

Rev. A.1.0

DESIGN

The JST136Q800D triac is suitable for general purpose AC switching applications in phase control applications, inductive motor starting circuits for phase control applications in light dimmer motor speed control etc. From T2 terminal to external heat sink. Page TO-126 is recommended.

PARAMETERS

ABBREVIATIONS

Parameter	Symbols	Value	Unit
Storage temperature range	T_{sg}	-40 150	
Operating temperature range	T_j	-40 125	
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{BM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	800	V
RMS on-state current ($T_c=83^\circ C$)	$I_{T(RMS)}$	4	A
Non-repetitive surge peak on-state current (full cycle, $t_p=20ms$, $T_j=25^\circ C$)	I_{TSM}	35	A
Non-repetitive surge peak on-state current (full cycle, $t_p=16.6ms$, $T_j=25^\circ C$)		38.5	
I^2t value for fusing ($t_p=10ms$, $T_j=25^\circ C$)	I^2t	6.1	A^2s

Gate current rise of on-state current ($I_G=2xI_{GT}$, $f=100Hz$, $T_j=125^\circ C$)

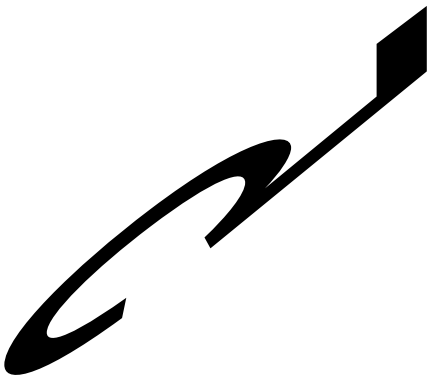
ELECTRICAL CHARACTERISTICS

(Tj=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
IGT	VD=12V RL=33	- -	MAX.	5	mA
				10	
VGT		ALL	MAX.	1	V
VGD	VD=VBM Tj=125 RL=3.3k	ALL	MIN.	0.2	V
IL	IG=1.2IGT	-	MAX.	15	mA
		-		25	
IH	IT=10mA		MAX.	15	(Refer to Tc 0 Tc 0 Tc 0)
dV/dt	VD=54V Gate n Tj=110		MIN.	10	V/s
(dV/dt)c	(dI/dt)c=1.8 A/ns Tj=110		MIN.	2.5	

FG1: Minimum power dissipation vs RMS on-state current

FG2: RMS on-state current vs case temperature



PARAM

Order code	Voltage V_{DRM} / V_{RM} (V)	IGT (mA)		Package	Base qty. (μ s)	Delivery mode
		-	-			
ST136Q-800D	800	5	10	TO-126	500	Bulk Pack

Document Revision History

#	Reason	Tags
Apr.14, 2023	A 1.0	Launched

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