

JIEJIE MICROELECTRONICS CO., LTD

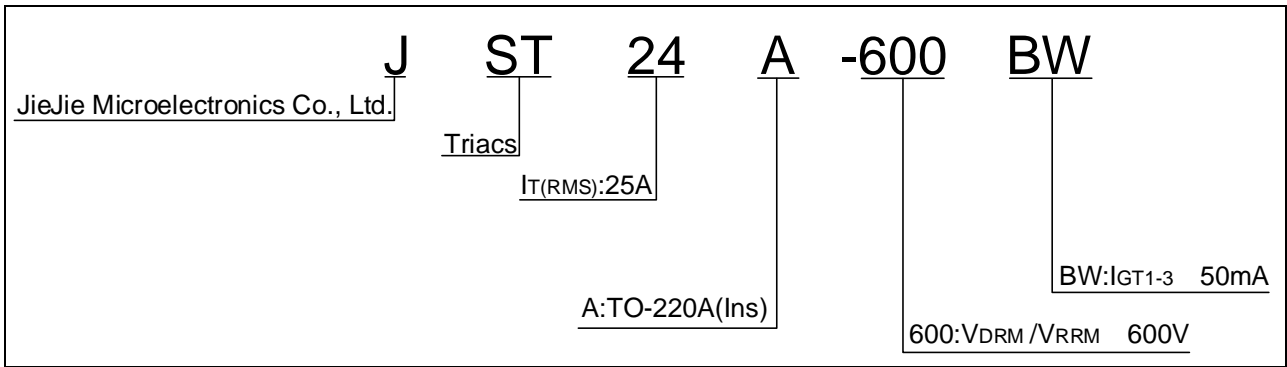
JST24A-600BW

Average gate power dissipation
Peak gate power
Peak pulse voltage ($T_j=25$; non-repetitive, off-state)

ELECTRICAL CHARACTERISTICS

Symbol	Test Condition
I_{GT}	$V_D=12V$ $R_L=33$
V_{GT}	
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$
I_L	$I_G=1.2I_{GT}$

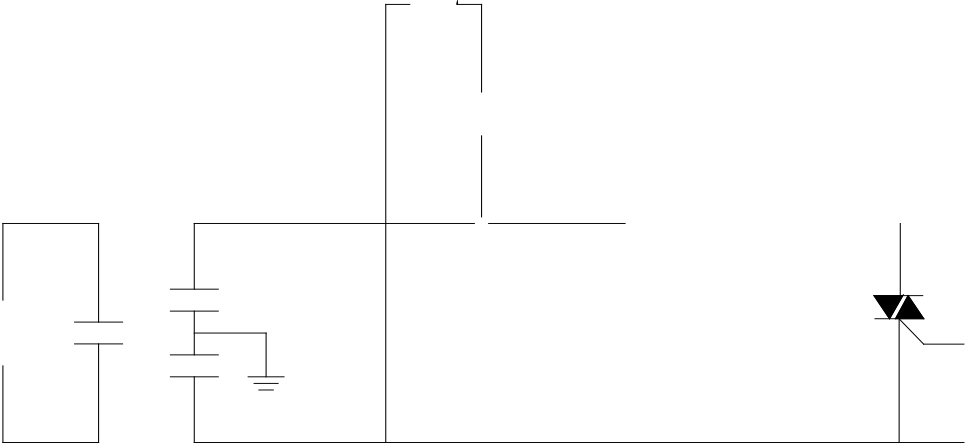
ORDERING INFORMATION



MARKING



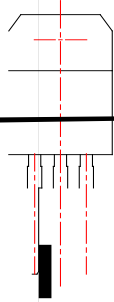
FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



JST24A-600BV

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PACKAGE MECHANICAL DATA



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